

AMENDMENTS TO THE CLAIMS

1. (Currently amended): A method in a data processing system including a storage device, said method comprising the steps of:

providing a write cache in said storage device; [[and]]

executing a FAST WRITE operation utilizing said write cache only when writing particular types of data, wherein a command complete status is returned prior to writing data during said FAST WRITE operation and a command complete status is returned only after writing data when said FAST WRITE operation is not executed[[.]];

receiving an instruction to write first data to said storage device;

determining whether said first data is sequential data;

in response to said first data being sequential data, determining whether said first data can be written within a particular time frame;

in response to a determination that said first data is sequential data that can be written within said particular time frame, returning a command complete status and then writing said first data to said storage device;

in response to a determination that said first data is sequential data that cannot be written within said particular time frame, writing said first data to said storage device and then returning a command complete status; and

in response to a determination that said first data is non-sequential data, writing said first data to said storage device and then returning a command complete status.

2. (Original): The method according to claim 1, further comprising the step of executing said FAST WRITE operation only when writing particular amounts of sequential data.

3. (Original): The method according to claim 1, further comprising the steps of:  
receiving an instruction to write first data to said storage device;  
determining whether said first data is said particular types of data;

in response to a determination that said first data is said particular types of data, returning a command complete status and then writing said first data to said storage device; and

in response to a determination that said first data is said not particular types of data, writing said first data to said storage device and then returning a command complete status.

4. (Canceled)

5. (Currently amended): The method according to claim 1 [[4]], further comprising the step of specifying said particular time frame, wherein said particular time frame is a minimum amount of time between a receipt of a power loss signal and a loss of power to said data processing system.

6. (Currently amended): The method according to claim 1 [[4]], wherein the step of determining whether said first data can be written within a particular time frame further comprises the step of determining an amount of time required to write said first data to said storage device.

7. (Original): The method according to claim 6, wherein said step of determining an amount of time required to write said first data to said storage device further comprises the steps of:

determining an amount of said first data;  
determining a physical location in said storage device where said first data is to be written; and  
determining an amount of time required for a read/write head to move to said physical location.

8. (Currently amended): A computer program product in a data processing system including a storage device, said computer program product comprising:

instruction means for providing a write cache in said storage device; [[and]]

instruction means for executing a FAST WRITE operation utilizing said write cache only when writing particular types of data, wherein a command complete status is returned prior to writing data during said FAST WRITE operation and a command complete status is returned only after writing data when said FAST WRITE operation is not executed[[.]];

instruction means for receiving an instruction to write first data to said storage device;

instruction means for determining whether said first data is sequential data;  
in response to said first data being sequential data, instruction means for determining whether said first data can be written within a particular time frame;  
in response to a determination that said first data is sequential data that can be written within said particular time frame, instruction means for returning a command complete status and then writing said first data to said storage device;

in response to a determination that said first data is sequential data that cannot be written within said particular time frame, instruction means for writing said first data to said storage device and then returning a command complete status; and

in response to a determination that said first data is non-sequential data,  
instruction means for writing said first data to said storage device and then returning a command complete status.

9. (Original): The product according to claim 8, further comprising instruction means for executing said FAST WRITE operation only when writing particular amounts of sequential data.

10. (Original): The product according to claim 8, further comprising:  
instruction means for receiving an instruction to write first data to said storage device;  
instruction means for determining whether said first data is said particular types of data;

in response to a determination that said first data is said particular types of data, instruction means for returning a command complete status and then writing said first data to said storage device; and

in response to a determination that said first data is said not particular types of data, instruction means for writing said first data to said storage device and then returning a command complete status.

11. (Cancelled)

12. (Currently amended): The product according to claim 8 [[11]], further comprising instruction means for specifying said particular time frame, wherein said particular time frame is a minimum amount of time between a receipt of a power loss signal and a loss of power to said data processing system.

13. (Currently amended): The product according to claim 8 [[11]], wherein said instruction means for determining whether said first data can be written within a particular time frame further comprises instruction means for determining an amount of time required to write said first data to said storage device.

14. (Original): The product according to claim 13, wherein said instruction means for determining an amount of time required to write said first data to said storage device further comprises:

instruction means for determining an amount of said first data;

instruction means for determining a physical location in said storage device where said first data is to be written; and

instruction means for determining an amount of time required for a read/write head to move to said physical location.

15. (Currently amended): A data processing system including a storage device, comprising:

a write cache in said storage device; [[and]]

a storage device controller for executing a FAST WRITE operation utilizing said write cache only when writing particular types of data, wherein a command complete status is returned prior to writing data during said FAST WRITE operation and a command complete status is returned only after writing data when said FAST WRITE operation is not executed[. . .];

said controller for receiving an instruction to write first data to said storage device;

said controller for determining whether said first data is sequential data;

in response to said first data being sequential data, said controller for determining whether said first data can be written within a particular time frame;

in response to a determination that said first data is sequential data that can be written within said particular time frame, said controller for returning a command complete status and then writing said first data to said storage device;

in response to a determination that said first data is sequential data that cannot be written within said particular time frame, said controller for writing said first data to said storage device and then returning a command complete status; and

in response to a determination that said first data is non-sequential data, said controller for writing said first data to said storage device and then returning a command complete status.

16. (Original): The system according to claim 15, further comprising said controller for executing said FAST WRITE operation utilizing said write cache only when writing particular amounts of sequential data.

17. (Original): The system according to claim 15, further comprising:

said controller for receiving an instruction to write first data to said storage device;

said controller for determining whether said first data is said particular types of data;

in response to a determination that said first data is said particular types of data, said controller for returning a command complete status and then writing said first data to said storage device; and

in response to a determination that said first data is said not particular types of data, said controller for writing said first data to said storage device and then returning a command complete status.

18. (Cancelled)

19. (Currently amended): The system according to claim 15 [[18]], further comprising a particular time frame, wherein said particular time frame is a minimum amount of time between a receipt of a power loss signal and a loss of power to said data processing system.

20. (Currently amended): The system according to claim 15 [[18]], wherein said controller for determining whether said first data can be written within a particular time frame further comprises said controller for determining an amount of time required to write said first data to said storage device.

21. (Original): The system according to claim 20, wherein said controller for determining an amount of time required to write said first data to said storage device further comprises:

    said controller for determining an amount of said first data;

    said controller for determining a physical location in said storage device where said first data is to be written; and

    said controller for determining an amount of time required for a read/write head to move to said physical location.